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Attorney Docket No. 5649-1258

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Joo et al.
Application Serial No.: 10/776,394
Filed: February 11, 2006
For: METHODS FOR FABRICATING FERROELECTRIC MEMORY DEVICES
WITH ENHANCED FERROELECTRIC PROPERTIES

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Issued: May 30, 2006

Date: September 12, 2006

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

**ERRORS APPEARING IN PRINTED PAGES
OF THE LETTERS PATENT**

Sir:

It is noted that certain errors appear in the Letters Patent identified above as follows:

Column 13,

Line 7 should read -- forming an upper insulating layer on the lower insulating --

The above errors are mistakes on the part of the Applicant but do not appear to merit a Certificate of Correction. It is respectfully requested, however, that this letter be placed on record in the file of this patent so that no question may arise as to the true meaning of the particular terms mentioned above as they appear in the patent.

Respectfully submitted,

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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on September 12, 2006.

Jessica M. French